

/ Descriptions

KF 50) CD GE G Silicon PNP transistor in a TO-92LM Plastic Package.

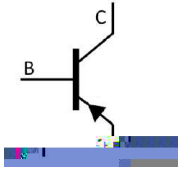
/ Features

) J; (/ - +
Low $V_{CE(sat)}$, complementary pair with 2SD1864.

/ Applications

General power amplifier applications.

/ Equivalent Circuit



/ Pinning



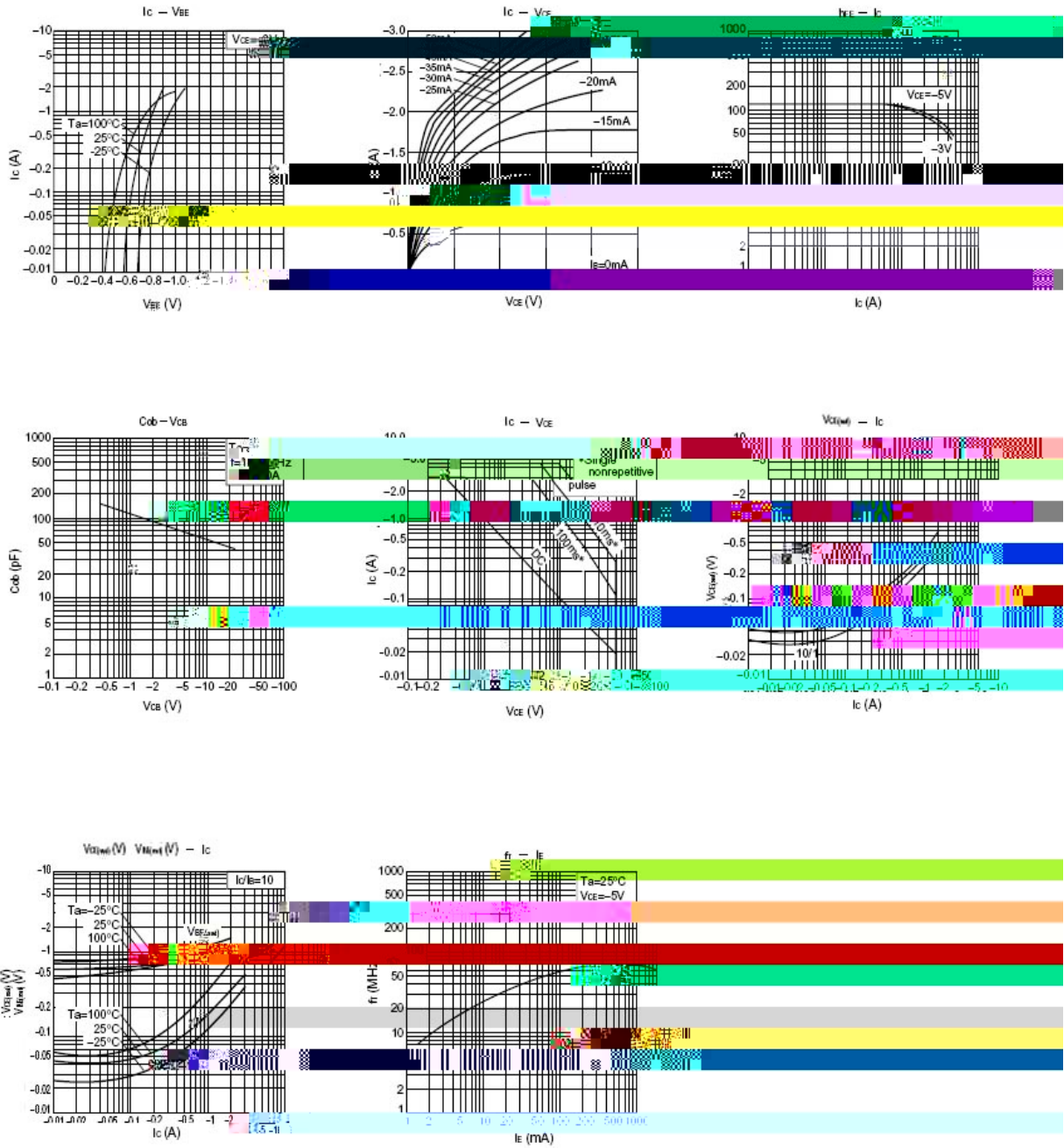
PIN1 Base PIN 2 Collector PIN 3 Emitter

/ h_{FE} Classifications & Marking

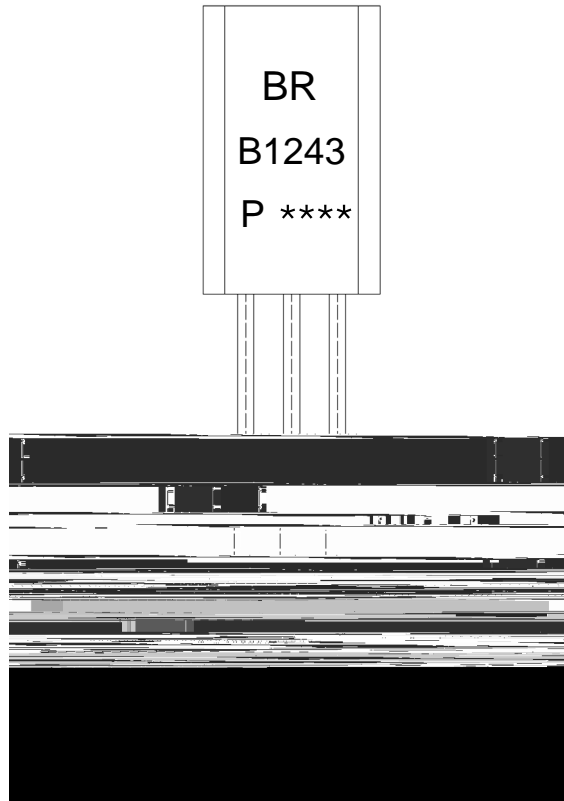
h_{FE} Classifications Symbol	P	Q	R
h_{FE} Range	82 180	120 270	180 390

Parameter	Symbol	Rating	Unit
Collector to Base Voltage	V_{CBO}	-60	V

/ Electrical Characteristic Curve



/ Marking Instructions



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G1 _=<

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Note:

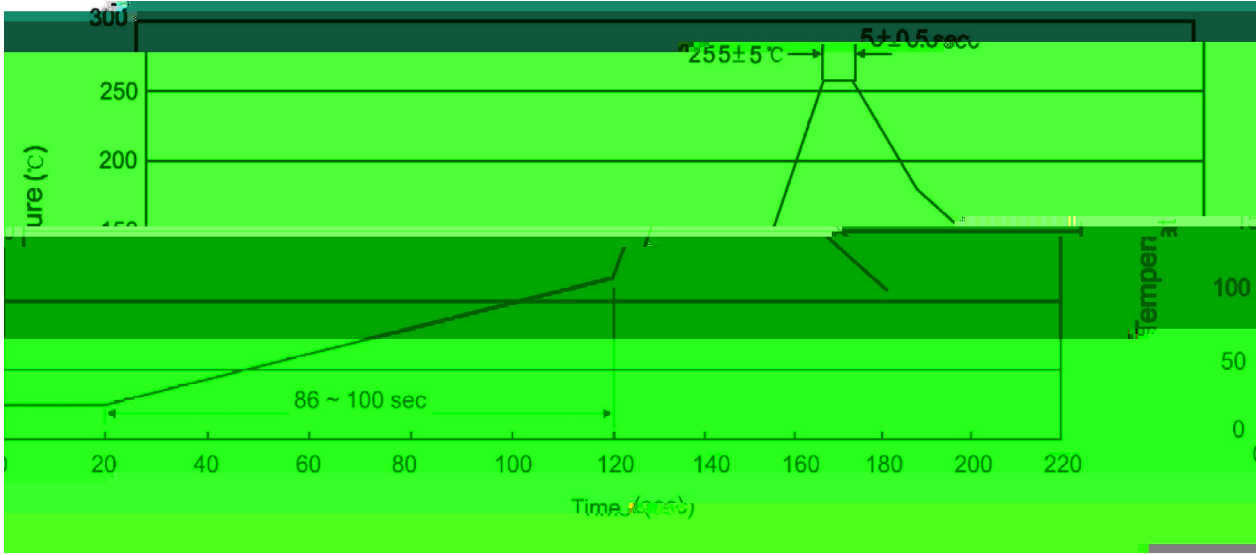
BR: Company Code.

B1243: Product Type.

P: h_{FE} Classifications Symbol

****: Lot No. Code,code change with Lot No.

() / Temperature Profile for Dip Soldering(Pb-Free)



Note:

- | | | | | | |
|---|--------|-----|------------|----------|---|
| 1 | 25 | 150 | 60 | 90sec; | 1.Preheating:25~150 , Time:60~90sec. |
| 2 | 255..5 | | 5..0.5sec; | | 2.Peak Temp.:255..5 , Duration:5..0.5sec. |
| 3 | | | 2 | 10 /sec. | 3. Cooling Speed: 2~10 /sec. |

/ Resistance to Soldering Heat Test Conditions

270..5 10..1 sec. Tf0.56Pb2fj/TT2 1 Tf2 0 TD-0.0005;