2SC2229

Rev.E Mar.-2016

蓝箭电子
BLUE ROCKET ELECTRONICS

DATA SHEET

/ Descriptions

WR0<50P ṂṂŁ QSQ ÆṀɥ s Ⅱ ᄀ Silicon NPN transistor in a TO-92LM Plastic Package.

/ Features

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High breakdown voltage, low output capacitance, high f_T.

/ Applications

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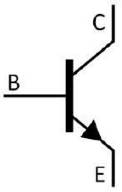
TV video output, high voltage switching and driver stage of audio amplifier applications.

/ Equivalent Circuit



/ Pinning

PIN1 χ Base　　PIN 2 χ Collector　　PIN 3 χ Emitter

/ h_{FE} Classifications & Marking

h_{FE} Classifications Symbol	O	Y
h_{FE} Range	70 140	120 240

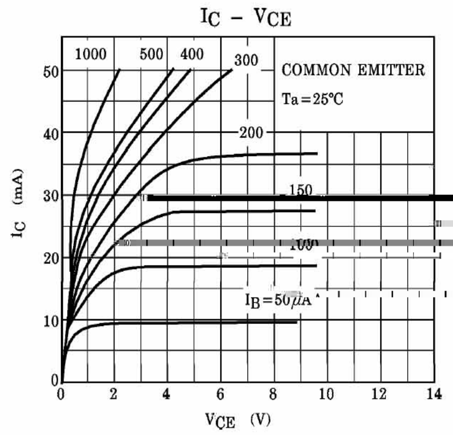
ѵ ῼ Parameter	ɣ ᴺ Symbol	ῼ Ӫ Rating	ᴅ ꞑ Unit
Collector to Base Voltage	V_{CBO}	200	V
Collector to Emitter Voltage	V_{CEO}	150	V
Emitter to Base Voltage	V_{EBO}	5.0	V
Collector Current (DC)	I_C	50	mA
Emitter Current	I_E	-50	mA
Collector Power Dissipation	P_C	800	mW
Junction Temperature	T_j	150	
Storage Temperature Range	T_{stg}	-55 150	

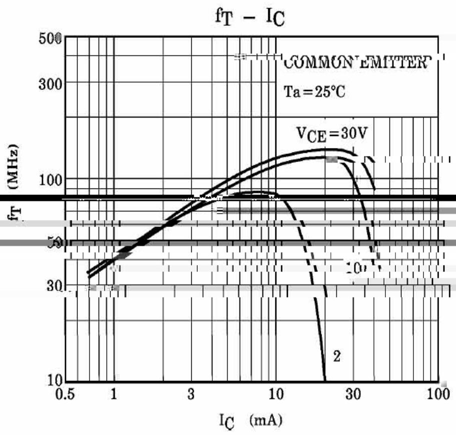
ѵ ῼ Parameter	ɣ ᴺ Symbol	╜ ፡ᛏ ꞇ Test Conditions	ῳ ṇ Ӫ Min	Ɛ ʒ Ӫ Typ	ῳ ᶾ Ӫ Max	ᴅ ꞑ Unit
Collector Cut-Off Current	I_{CBO}	V_{CB}=200V I_E=0			0.1	μA
Emitter Cut-Off Current	I_{EBO}	V_{EB}=5.0V I_C=0			0.1	μA

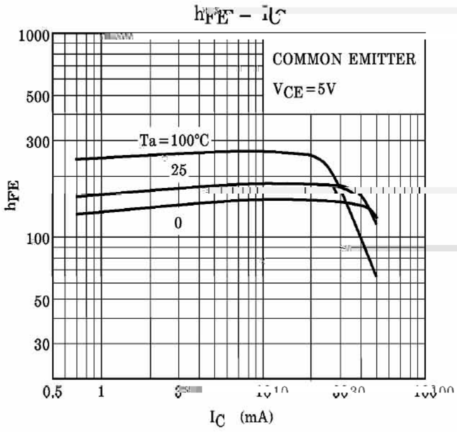
DC Current Gain h_{FE}

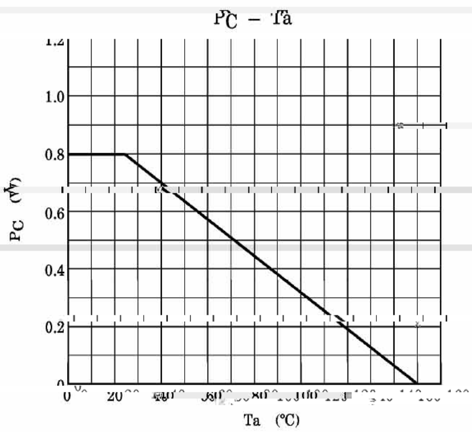
Typ A

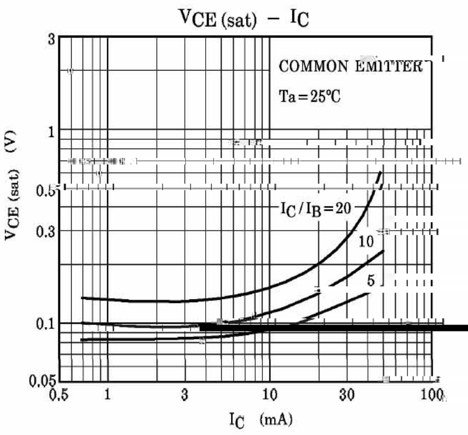
/ Electrical Characteristic Curve

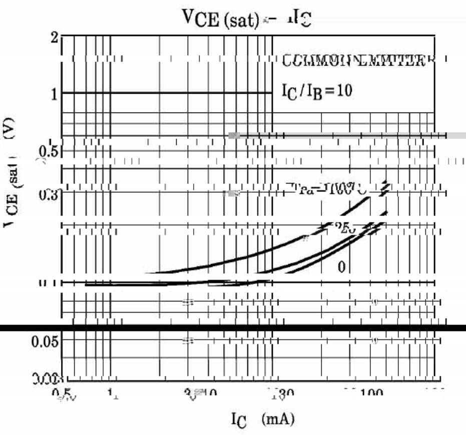






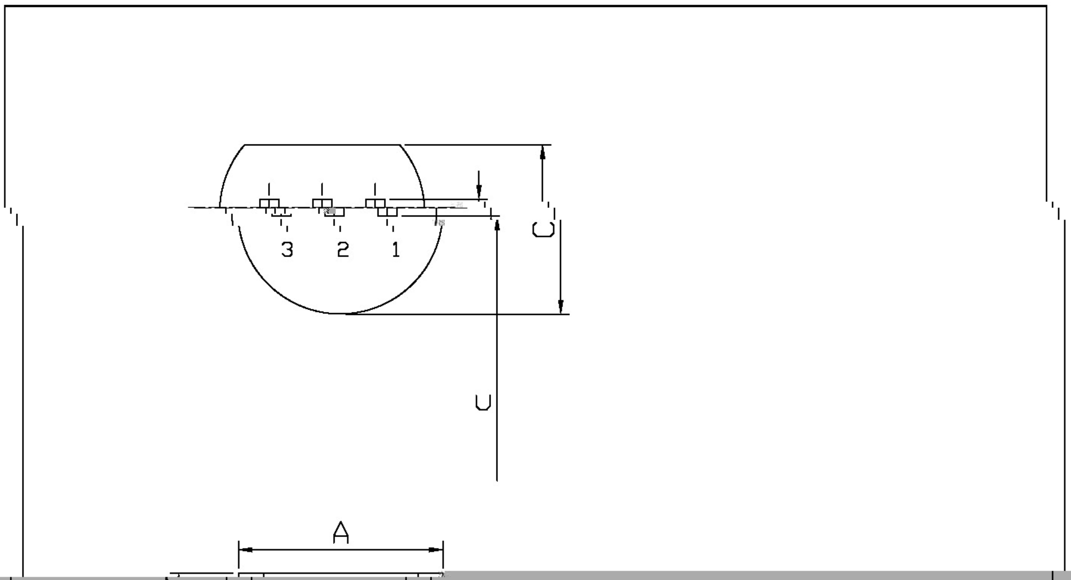






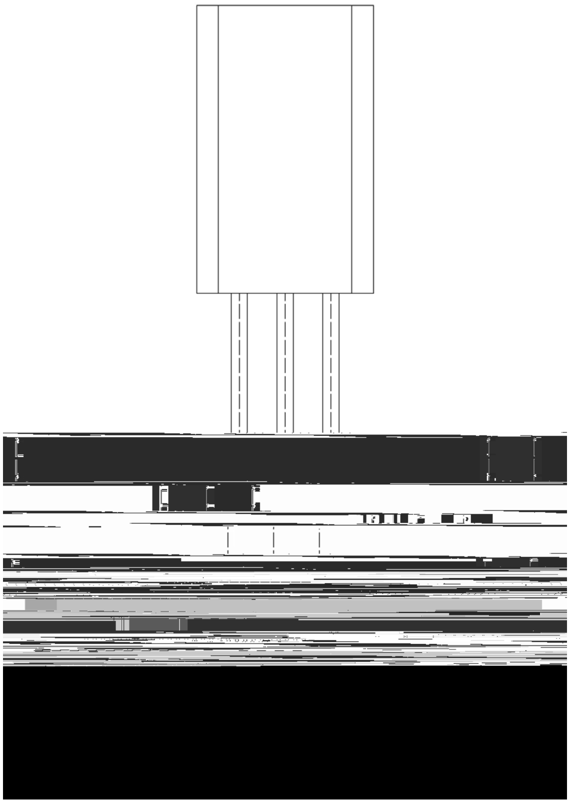
/ Package Dimensions

TO-92LM Unit: mm



Symbol	Dimensions In Millmeters	
	Min	Max
A	4.7	5.1
[illegible]	[illegible]	[illegible]
[illegible]	[illegible]	[illegible]
[illegible]	[illegible]	[illegible]

/ Marking Instructions



ب ˝ χ

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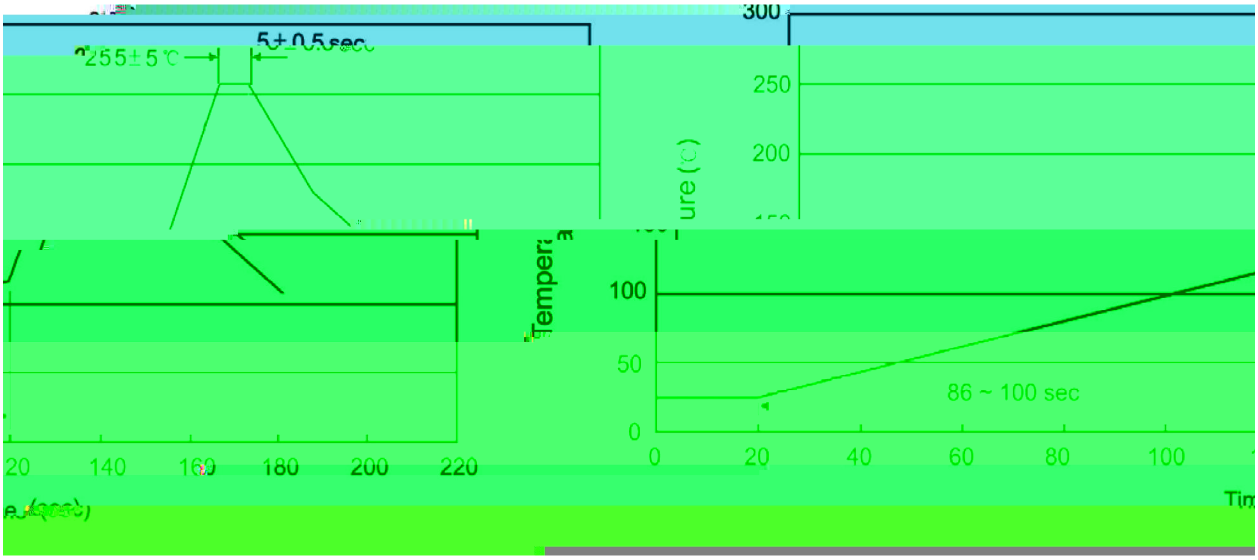
F555< χ ѭ ʒ ᴺ Ђ

(   ) /   Temperature Profile for Dip Soldering(Pb-Free)



ب ̋ χ

1 ὶ ♬ □ ẙ 25 Ḱ 150 ɬ ı ̀H ڙ 60 Ḱ 90sec;

2 Ś Ӫ □ ẙ 255 ° 5 ɬ ı ̀H ڙ ̔Y ᛅ ᵺ 5 ° 0.5sec;

3   ì ˙ ˥ ˛ ɔ ه ẙ ᵺ 2 Ḱ 10 ɬ /sec.

Note:

1.Preheating:25~150 ɬ , Time:60~90sec.

2.Peak Temp.:255 ° 5 ɬ , Duration:5 ° 0.5sec.

3. Cooling Speed: 2~10 ɬ /sec.

/   Resistance to Soldering Heat Test Conditions

□ ẙ χ 270 ° 5 ɬ   ̀H ڙ χ 10 ° 1 sec.

Temp.:270±5   Time:10±1 sec

/ Packaging SPEC.

̏Ω ʈ ɣ ㇾ / BULK