

/ Descriptions

VRW056

SQS

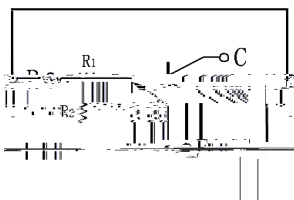
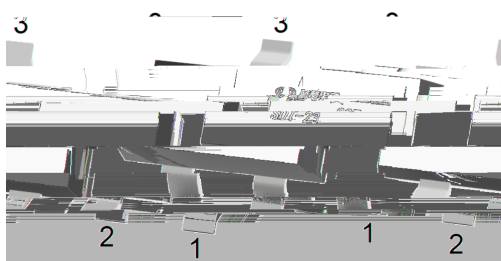
Silicon PNP Digital transistor in a SOT-23 Plastic Package.

/ Features

With built-in bias resistors, simplify circuit design, reduce a quantity of parts and manufacturing process,HF Product.

/ Applications

Switching, inverter circuit, interface circuit and driver circuit applications.

/ Equivalent Circuit**/ Pinning**

PIN 1 Base

PIN 2 Emitter

PIN 3 Collector

/ h_{FE} Classifications & Marking

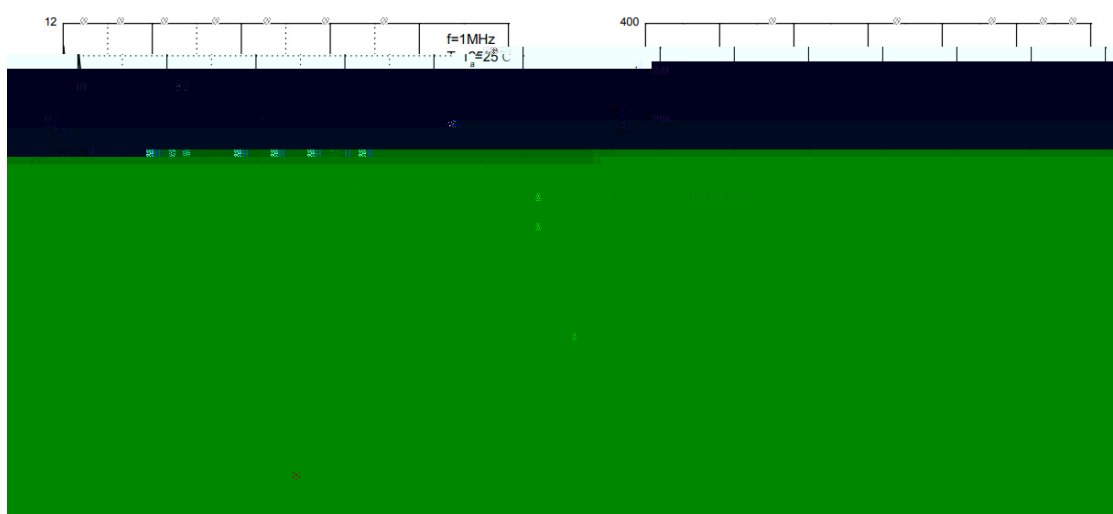
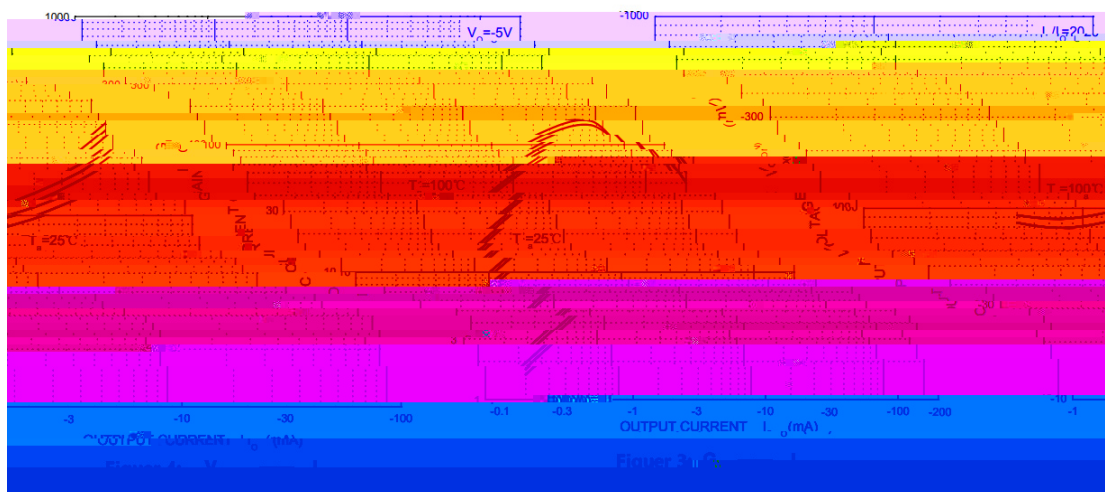
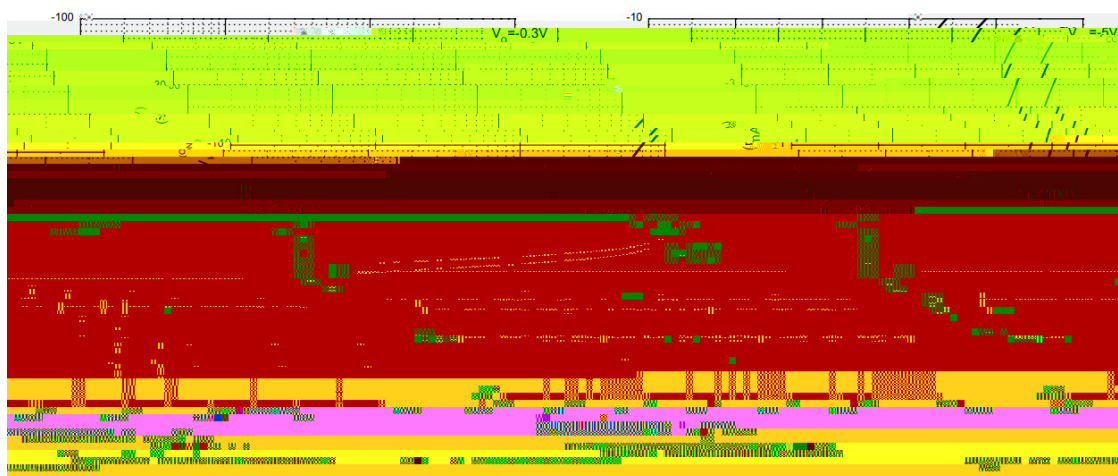
Marking	H13
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Parameter	Symbol	Rating	Unit
Output Voltage	V_{CC}	-50	V
Input Voltage	V_{IN}	10	V
		-30	V
Output Current	I_O	-100	mA
Power Dissipation	P_d	200	mW
Junction Temperature	T_j	150	
Storage Temperature Range	T_{stg}	-55 150	

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Input Voltage	$V_{I(off)}$	$V_{CC}=-5.0V$ $I_O=-100\mu A$	-0.5		-	V
	$V_{I(on)}$	$V_O=-0.3V$ $I_O=-20mA$			-3	V
Output Voltage	$V_{O(on)}$	$I_O=-10mA$ $I_I=-0.5mA$		-0.1	-0.3	V
Input Current	I_I	$V_I=-5.0V$			-1.8	mA
Output Cut-off Current	$I_{O(off)}$	$V_{CC}=-50V$ $V_I=0V$			-0.5	μA
DC Current Gain	G_I	V				

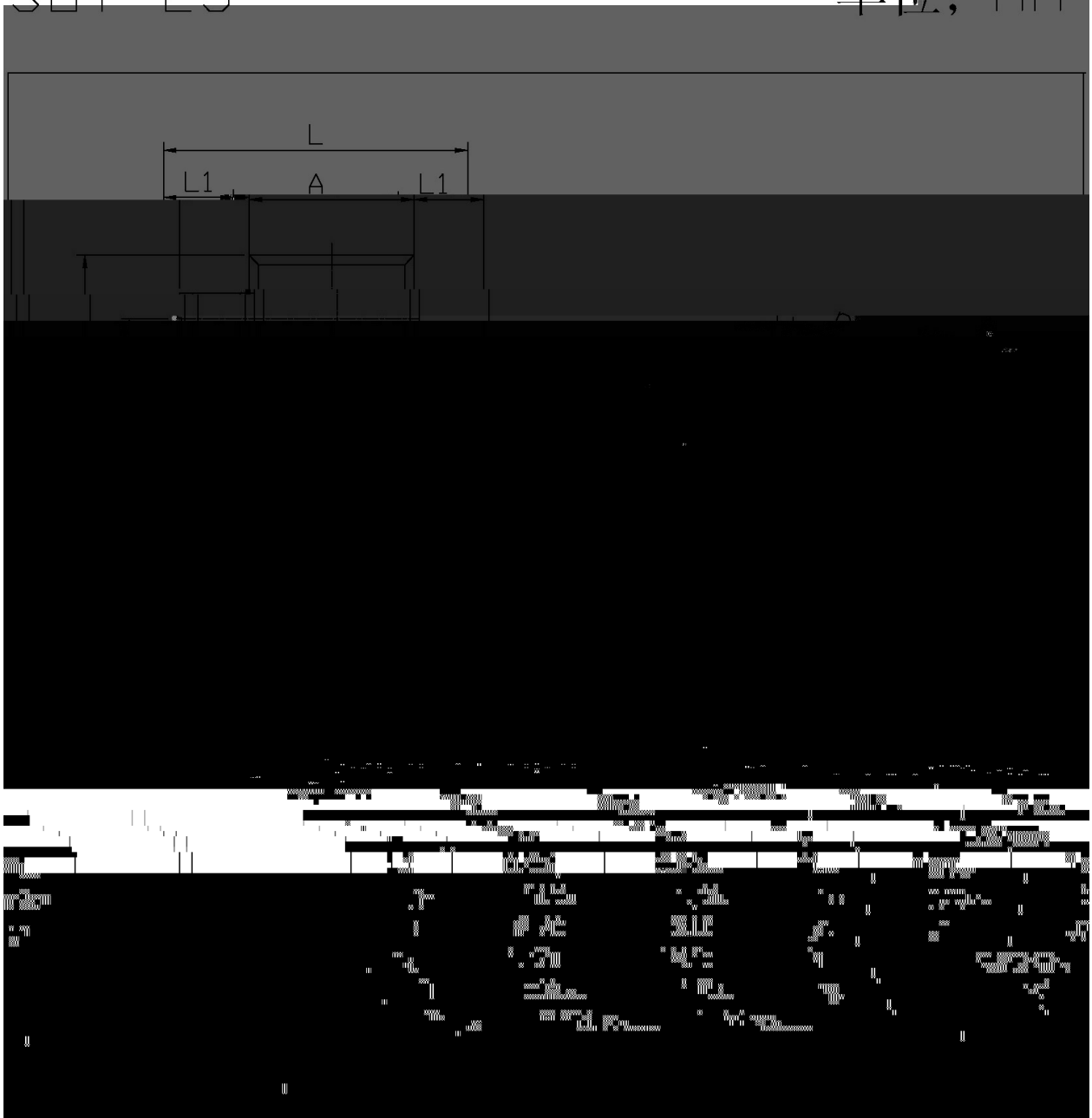
/ Electrical Characteristic Curve



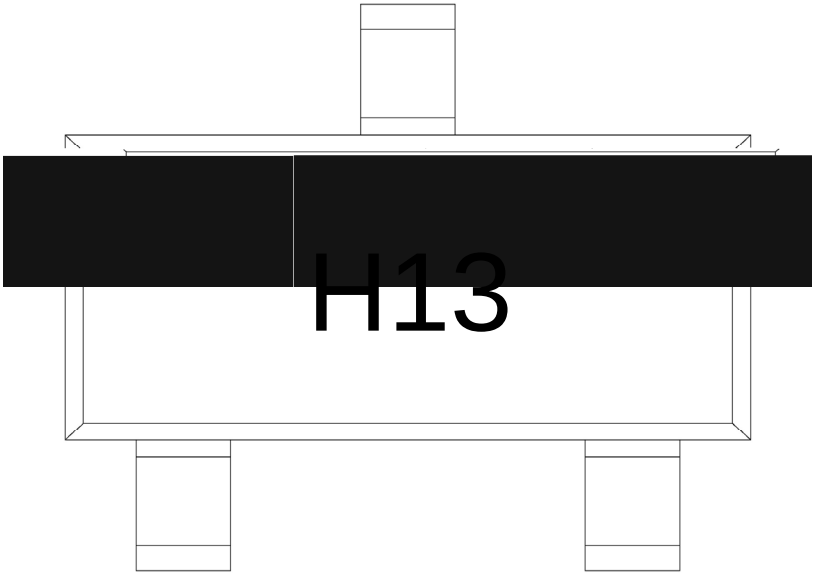
/ Package Dimensions

SOT-23

单位: mm



/ Marking Instructions



K
13
Note:
K Company Code
13 Product Type Code

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- 1

150 180%

60 90sec;
- 2

245[±]5%

5[±]0.5sec;
- 3

2 10%/sec.
- Note:
1.Preheating:150~180%, Time:60~90sec.
2.Peak Temp.:245[±]5%, Duration:5[±]0.5sec.
3. Cooling Speed: 2~10%/sec.

/ Resistance to Soldering Heat Test Conditions

605[±]5% (/ -5.7(Packagling)24286(SPEC.())J/T7 1 Tf03.4485 32.3713 TD.0004 Tc-35.65309:6025130