

/ Descriptions

Silicon PNP transistor in a TO-220F Plastic Package.

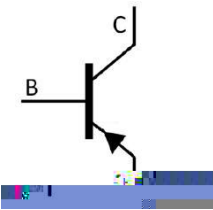
/ Features

Good Linearity of h_{FE} , complementary pair with KTD2060.

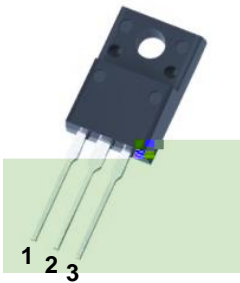
/ Applications

General Purpose Application.

/ Equivalent Circuit



/ Pinning



PIN1 Base PIN 2 Collector PIN 3 Emitter

/ h_{FE} Classifications & Marking

h_{FE} Classifications Symbol	R	O	Y
h_{FE} Range	40~80	70~140	120~240

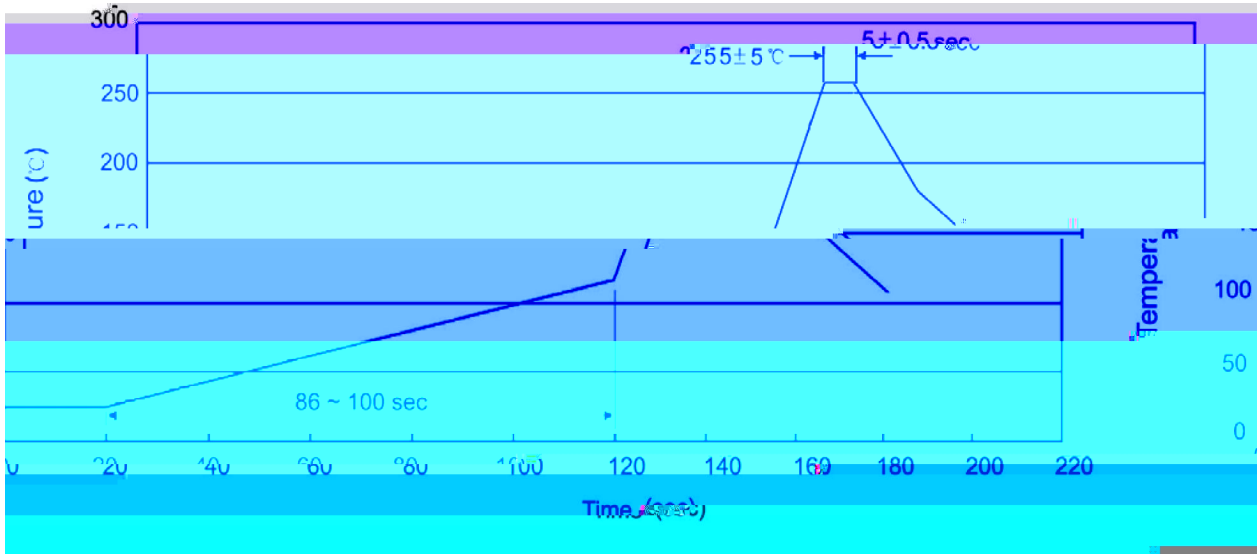
Parameter	Symbol	Rating	Unit
Collector to Base Voltage	V_{CBO}	-80	V
Collector to Emitter Voltage	V_{CEO}	-80	V
Emitter to Base Voltage	V_{EBO}	-5	V
Collector Current - Continuous	I_C	-4	A
Base Current	I_B	-0.4	A
Collector Power Dissipation	$P_C(T_C=25)$	25	W
Junction Temperature	T_j	150	
Storage Temperature Range	T_{stg}	-55 150	

Parameter	Symbol	Test Conditions	Min	Typ	Max

/ Package Dimensions



() / Temperature Profile for Dip Soldering(Pb-Free)



Note:

- | | | | | | |
|---|-----|-----|----|----------|---|
| 1 | 25 | 150 | 60 | 90sec; | 1.Preheating:25~150 , Time:60~90sec. |
| 2 | 255 | 5 | 5 | 0.5sec; | 2.Peak Temp.:255 5 , Duration:5 0.5sec. |
| 3 | | | 2 | 10 /sec. | 3. Cooling Speed: 2~10 /sec. |