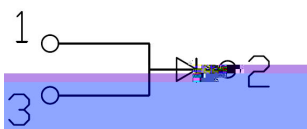


/ Descriptions

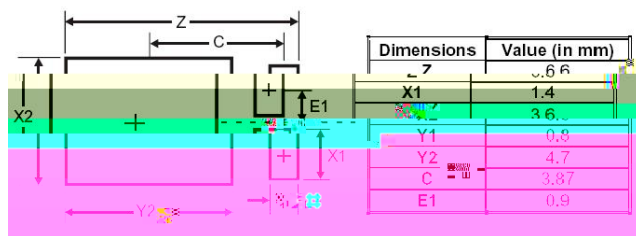
/ Features

/ Applications

/ Equivalent Circuit



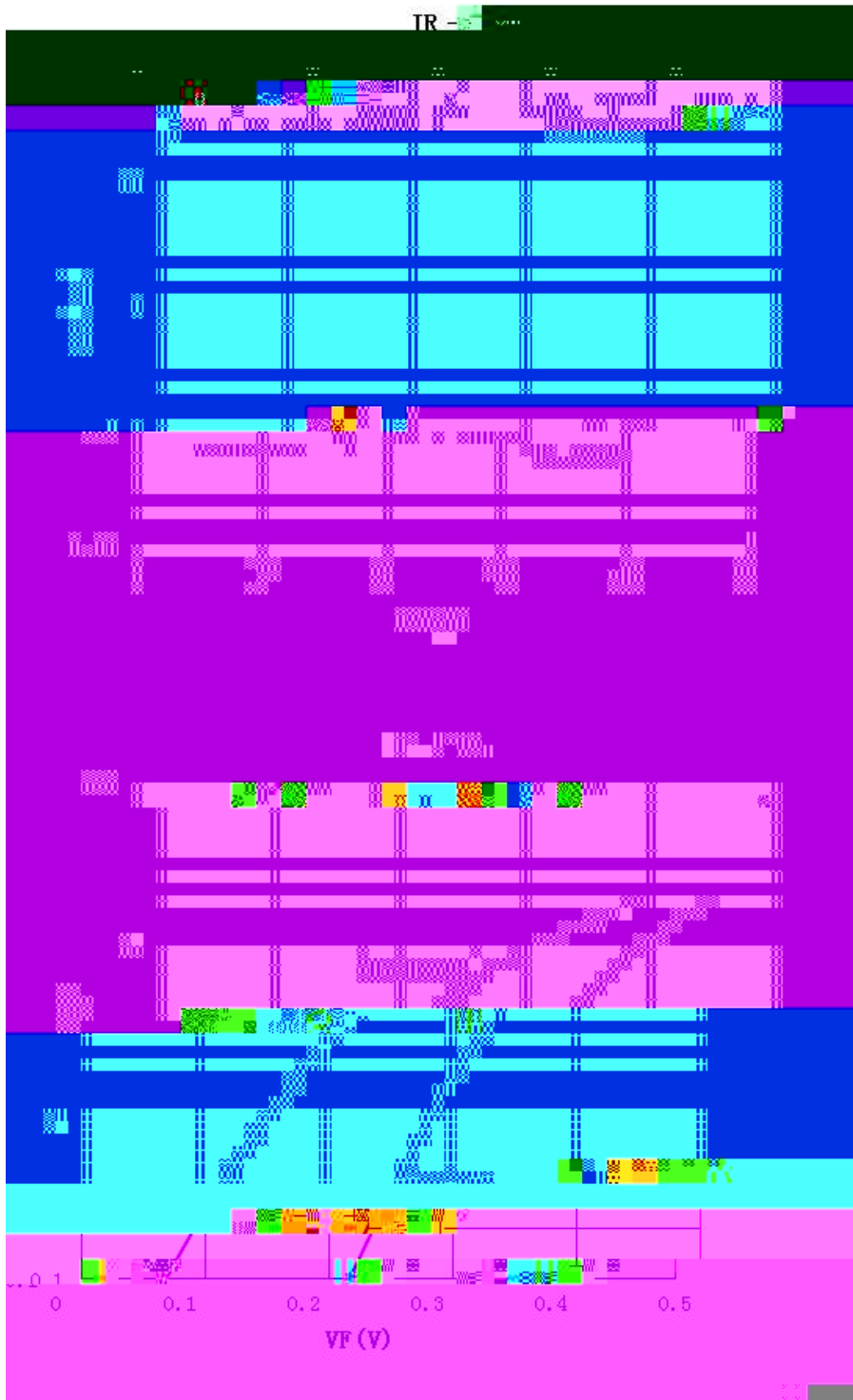
/ Pinning



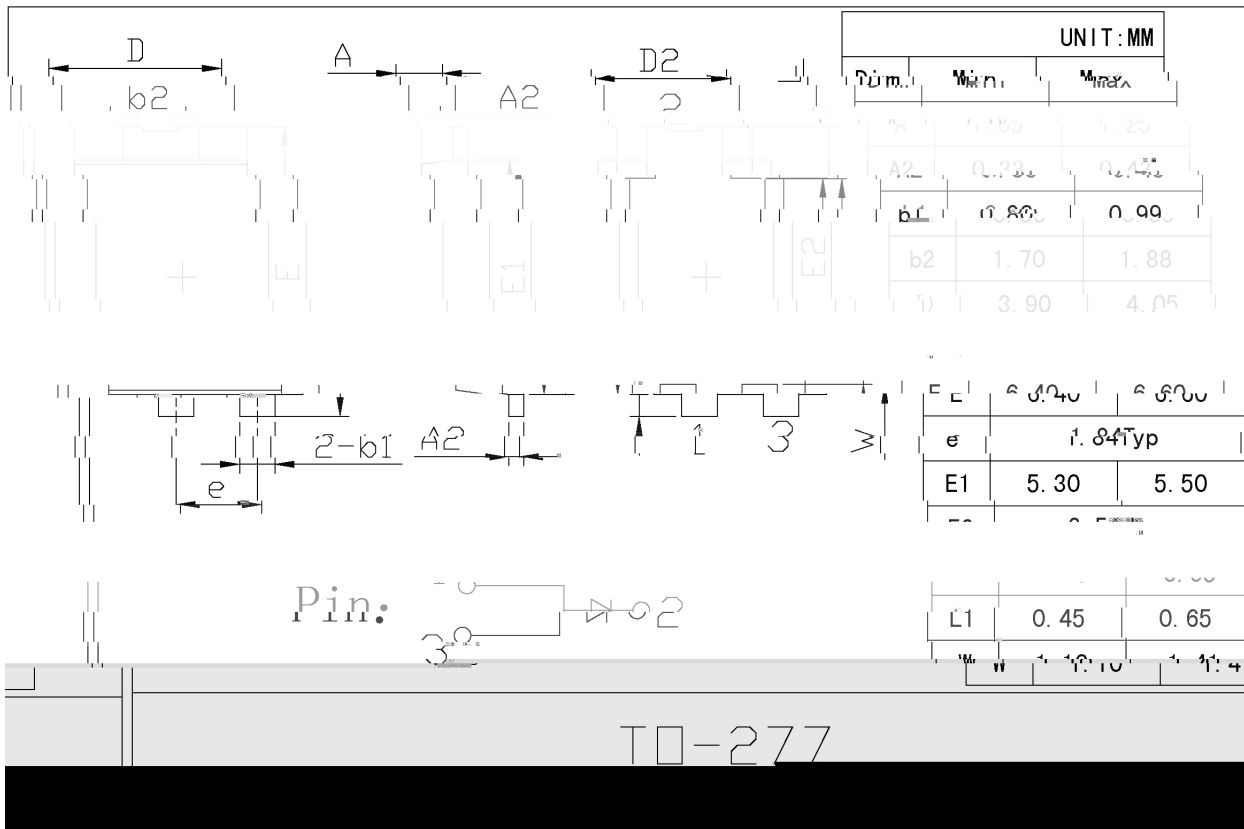
/ h_{FE} Classifications & Marking

LV20S50E

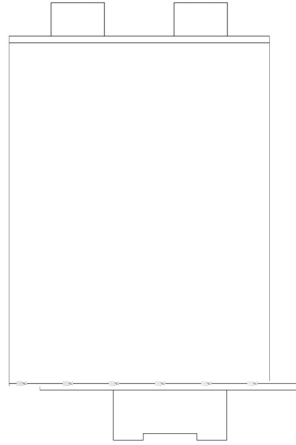
/ Electrical Characteristic Curve



/ Package Dimensions



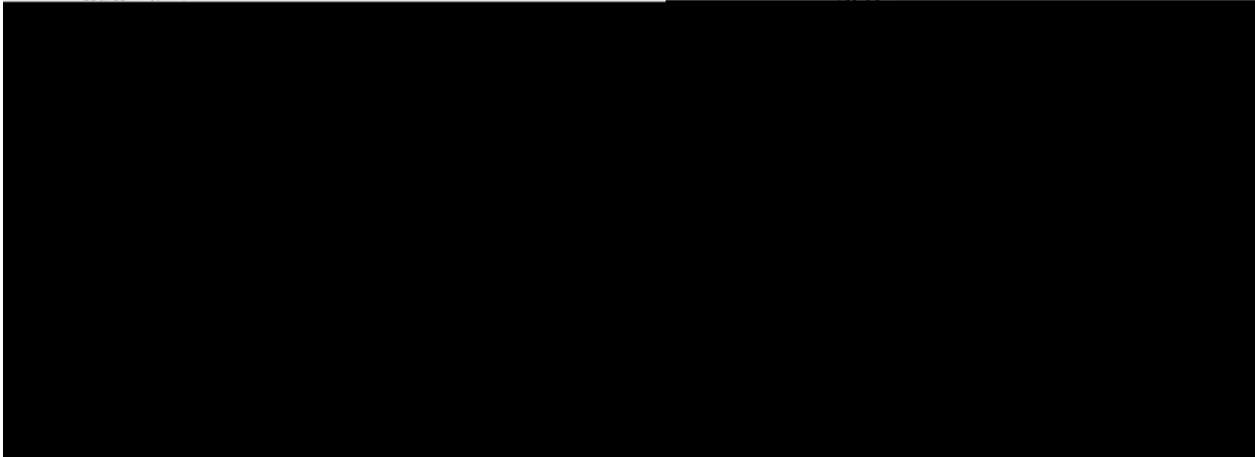
/ Marking Instructions



20S50

20S50

() / Temperature Profile for IR Reflow Soldering(Pb-Free)



± ± ± ±

/ Resistance to Soldering Heat Test Conditions

± ±

/ Packaging SPEC.

/ Notices