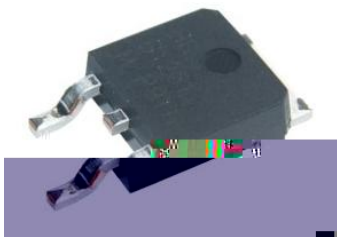
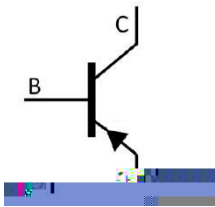


TO-252          PNP          Silicon PNP transistor in a TO-252 Plastic Package.

High voltage, low saturation voltage, low collector output capacitance, complementary pair with MPSA42D.

High voltage control circuit.



PIN1 Base          PIN 2,4 Collector          PIN 3 Emitter

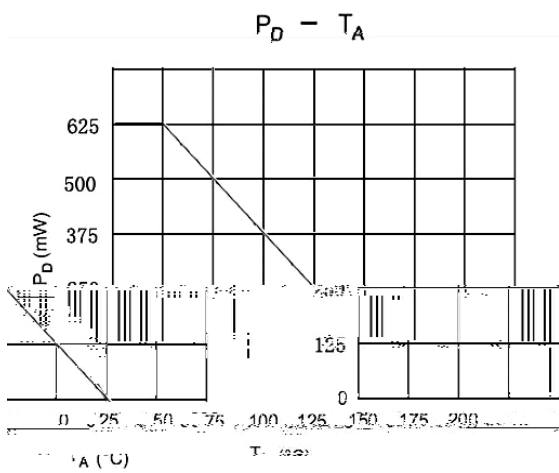
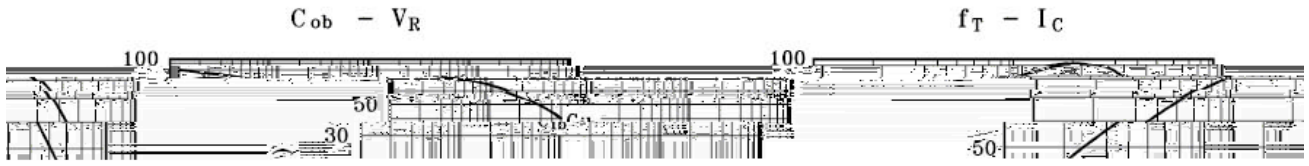
### / $h_{FE}$ Classifications & Marking

See Marking Instructions.

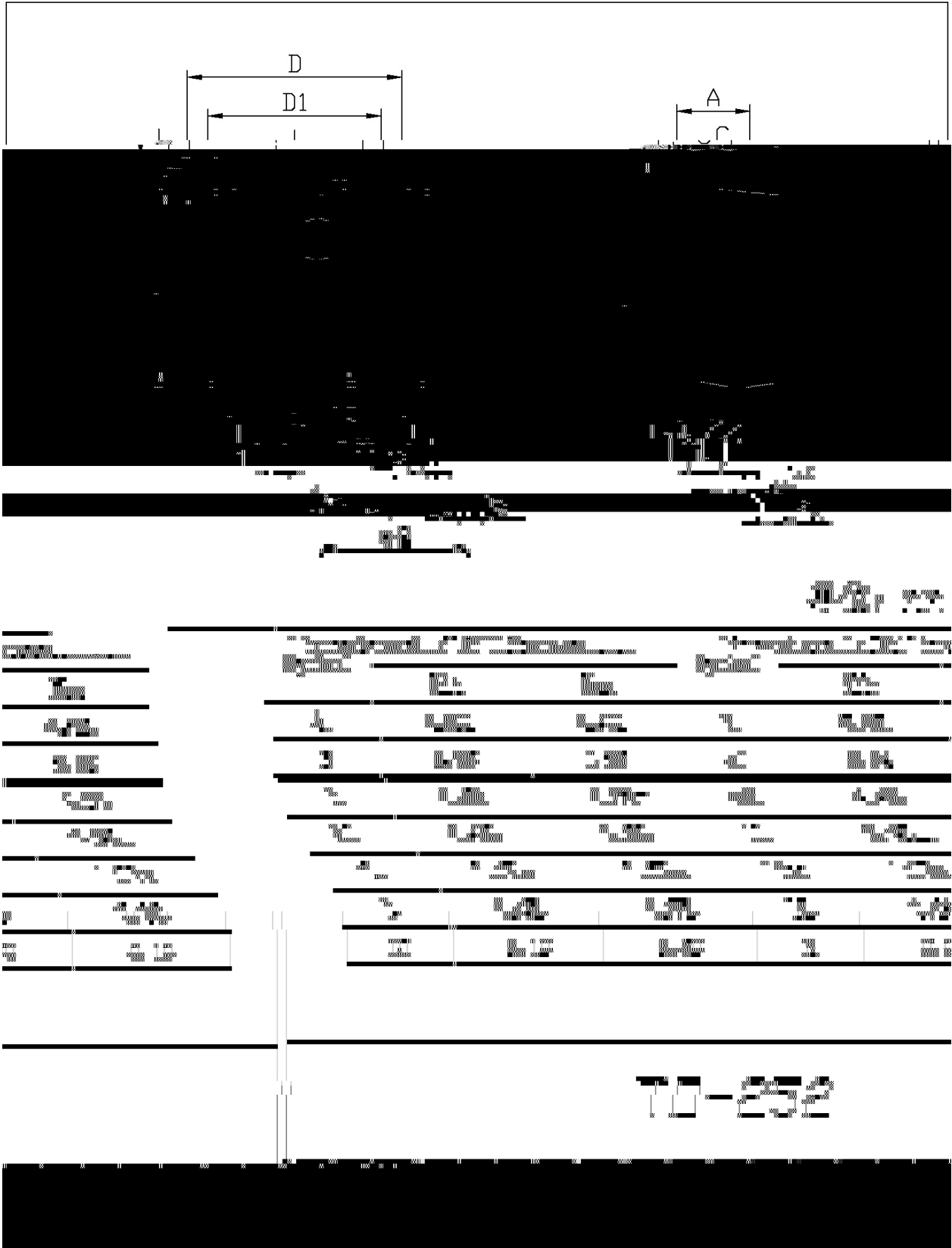
Parameter	Symbol	Rating	Unit
Collector to Base Voltage	$V_{CBO}$	-300	V
Collector to Emitter Voltage	$V_{CEO}$	-300	V
Emitter to Base Voltage	$V_{EBO}$	-5.0	V
Collector Current - Continuous	$I_C$	-500	mA
Collector Power Dissipation	$P_C$	625	mW
Junction Temperature	$T_j$	150	
Storage Temperature Range	$T_{stg}$	-55 150	

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Collector to Base Breakdown Voltage	$V_{CBO}$	$I_C = -100\mu A$ $I_E = 0$	-300			V
Collector to Emitter Breakdown Voltage	$V_{CEO}$					

/ Electrical Characteristic Curve



/ Package Dimensions



/ Marking Instructions



BR

A 92D

\*\*\*\*

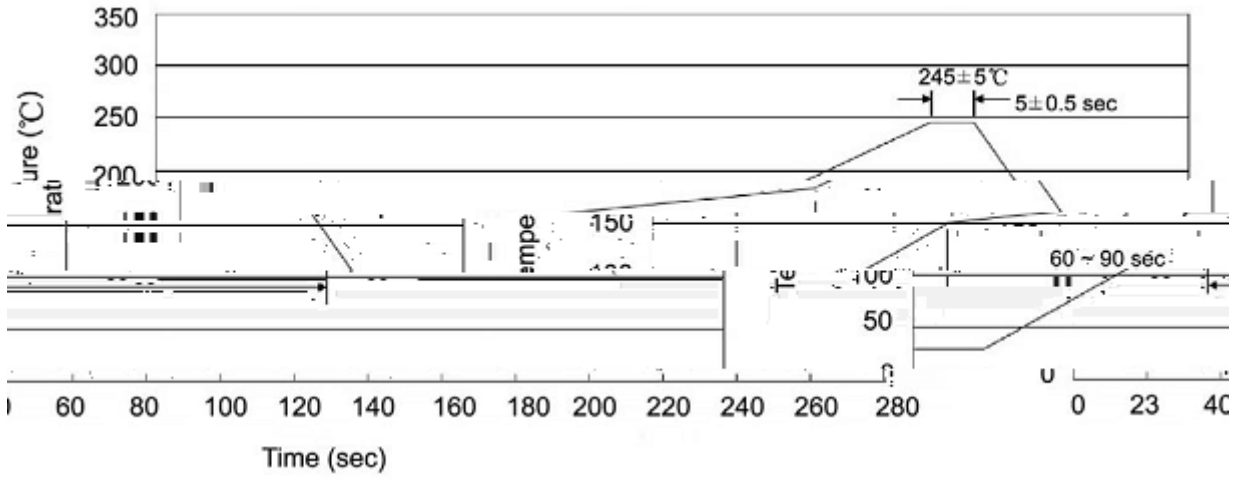
Note:

BR: Company Code

A92D: Product Type.

\*\*\*\*: Lot No. Code, code change with Lot No.

( ) / Temperature Profile for IR Reflow Soldering(Pb-Free)



Note:

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