

# MPSA94D

Rev.A Jun.-2016



DATA SHEET

## / Descriptions

KF \$ , ) GE G Silicon PNP transistor in a TO-252 Plastic Package.

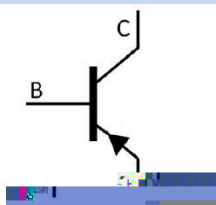
## / Features

High voltage.

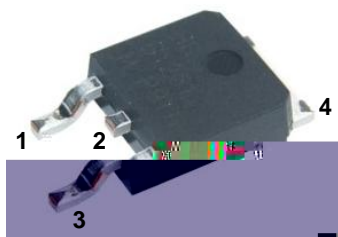
## / Applications

High voltage control circuit.

## / Equivalent Circuit



## / Pinning



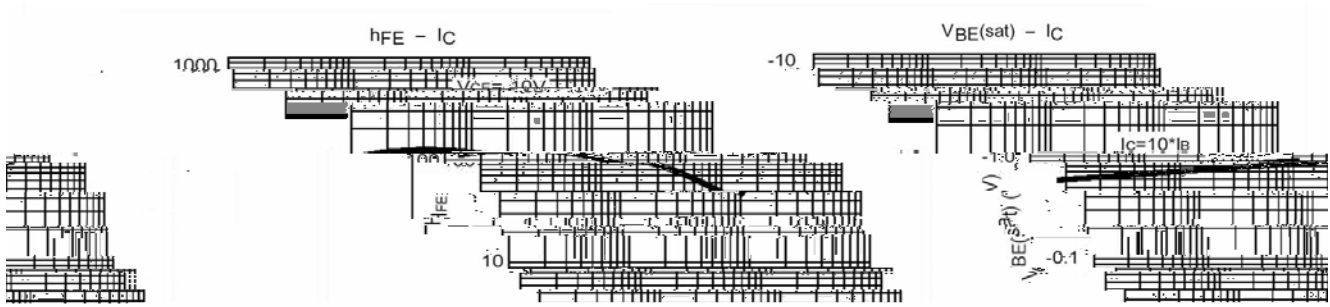
PIN1 Base      PIN 2,4 Collector      PIN 3 Emitter

## / hFE Classifications & Marking

See Marking Instructions.

Parameter	Symbol	Rating	Unit
Collector to Base Voltage	$V_{CBO}$	-400	V
Collector to Emitter Voltage	$V_{CEO}$	-400	V
Emitter to Base Voltage	$V_{EBO}$		

/ Electrical Characteristic Curve

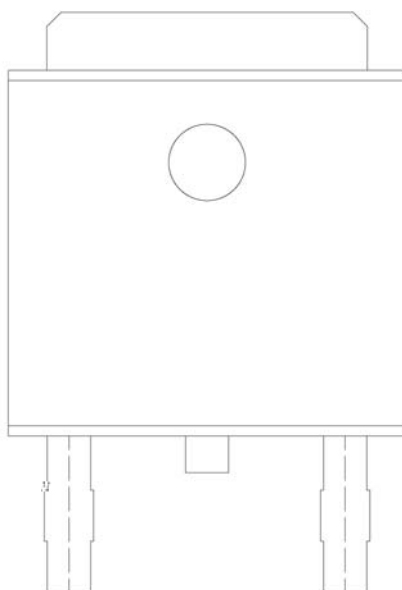


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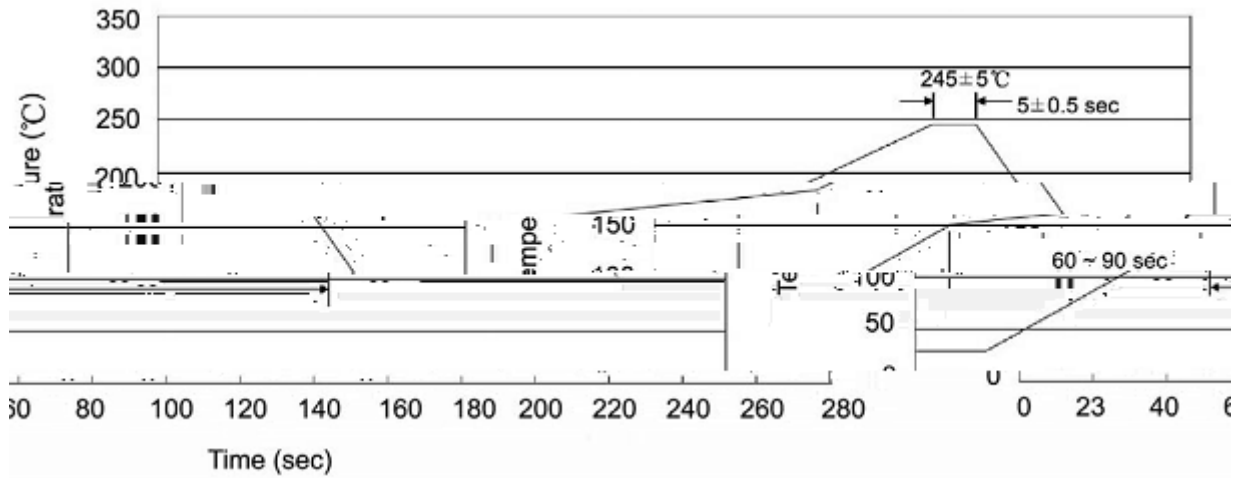
**DATA SHEET**

**/ Marking Instructions**



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80+;

( ) / Temperature Profile for IR Reflow Soldering(Pb-Free)



Note:

- 1. 25 150 60 90sec; 1.Preheating:25~150 , Time:60~90sec.
- 2. 245..5 5..0.5sec; 2.Peak Temp.:245..5 , Duration:5..0.5sec.
- 3. 2 10 /sec. 3. Cooling Speed: 2~10 /sec.

/ Resistance to Soldering Heat Test Conditions

260..5 10..1 sec. Temp.:260±5 Time:10±1 sec

/ Packaging SPEC.

/ REEL

Package Type	Units					Dimension (unit mm <sup>3</sup> )		
	Units/Reel	Reels/Inner Box	Units/Inner Box	Inner Boxes/Outer Box	Units/Outer Box	Reel	Inner Box	Outer Box
TO-252	2,500	2	5,000	5	25,000	13 ×16	360×360×50	385×257×392

/ TUBE

Package Type	Units					Dimension (unit mm <sup>3</sup> )		
	Units/Tube	Tubes/Inner Box	Units/Inner Box	Inner Boxes/Outer Box	Units/Outer Box	Tube	Inner Box	Outer Box
TO-251/252	75	48	3,600	5	18,000	526×20.5×5.25	555×164×50	575×290×180